# Structural Changes in Nickel Silicide Thin Films under the Presence of Al and Ga

Online: 2010-01-12

A. Mogilatenko<sup>1,a</sup>, F. Allenstein <sup>2,b</sup>, M.A. Schubert<sup>2,c</sup>, M. Falke<sup>2,d</sup>, G. Beddies<sup>2,e</sup> and W. Neumann<sup>1,f</sup>

Keywords: silicides, Nickel silicide, Aluminium, Gallium, thin films

**Abstract.** Thin Ni/Al and Ni/Ga layers of different atomic ratios were codeposited onto Si(001) at room temperature followed by subsequent annealing. Influence of annealing temperature on morphology and composition of ternary disilicide NiSi<sub>2-x</sub>Al<sub>x</sub> and NiSi<sub>2-x</sub>Ga<sub>x</sub> layers was investigated by transmission electron microscopy. Addition of Al or Ga leads to a decrease of the disilicide formation temperature from 700°C down to at least 500°C. Depending on the composition closed, uniformly oriented NiSi<sub>2-x</sub>Al<sub>x</sub> and NiSi<sub>2-x</sub>Ga<sub>x</sub> layers were observed after annealing at 900°C, whereas reaction of a pure Ni film with Si leads to the island formation with a mixture of A- and B-type orientations.

#### Introduction

In the recent decades strong interest has been drawn to the class of materials known as silicides [1], which are chemical compounds of silicon with different metals. Among them metallic silicides are of major importance for microelectronic devices. Their electrical properties as well as their excellent compatibility with Si have made them attractive for the use as ohmic contacts, Schottky barrier contacts, local interconnects and gate electrodes.

Formation of nickel silicide thin films on Si(001) has been intensively investigated, since the low resistant monosilicide phase NiSi is an attractive candidate as a contact material in Si-based ultralarge scale integration [2]. On the other hand the disilicide NiSi<sub>2</sub> phase is interesting due to its cubic structure (CaF<sub>2</sub> structure type), which allows the growth of epitaxial layers on Si substrates [3]. Among all transition metal silicides NiSi<sub>2</sub> shows the smallest lattice mismatch of -0.4 % to Si. Despite the similarity of the crystal structures and the extremely low lattice mismatch the growth of closed uniformly oriented NiSi<sub>2</sub> layers on Si(001) is a difficult task.

The first problem arising during the NiSi<sub>2</sub> formation is fabrication of closed silicide layers. Ni atoms are the dominant diffusion species in NiSi<sub>2</sub> formation [4]. Additionally, Ni bulk diffusion in Si strongly dominates its surface diffusion [5]. The high diffusivity of Ni atoms in Si prevents the formation of closed NiSi<sub>2</sub> layers and leads to the island formation at high substrate temperatures [6]. The second problem is the formation of uniformly oriented layers. Generally, NiSi<sub>2</sub> grows simultaneously in two orientations on Si, which are known as A-type and B-type orientations [7]. A-type orientation is formed when NiSi<sub>2</sub> crystal structure has the same orientation as the Si substrate. The B-type orientation (also called twin orientation) represents A-type crystallites mirrored on one of the Si{111} planes.

<sup>&</sup>lt;sup>1</sup> Humboldt University of Berlin, Institute of Physics, Newtonstr. 15, 12489 Berlin, Germany

<sup>&</sup>lt;sup>2</sup> Chemnitz University of Technology, Institute of Physics, 09107 Chemnitz, Germany

<sup>&</sup>lt;sup>3</sup> Bruker AXS Microanalysis GmbH, Schwarzschildstrasse 12, 12489 Berlin, Germany annog@physik.hu-berlin.de, bfrank.allenstein@physik.tu-chemnitz.de, candreas.schubert@s2000.tu-chemnitz.de, dMeiken.Falke@bruker-axs.de, beddies@physik.tu-chemnitz.de, fwolfgang.neumann@physik.hu-berlin.de

The third problem occurs during the NiSi<sub>2</sub> growth on technologically relevant Si(001) substrates. Commonly, NiSi<sub>2</sub> layers with planar atomically smooth interfaces are formed on Si(111) [8], whereas the epitaxial growth on Si(001) is accompanied by favorable formation of NiSi<sub>2</sub>{111}/Si{111} interfaces, which results in formation of pyramidal islands [8, 9].

It is possible to influence structural and physical properties of thin films by adding a substituting element, i.e. obtaining a ternary silicide. A number of authors analyzed thermal stability of nickel monosilicide phase, NiSi, during a solid state reaction of a number of metal/Ni bilayers on Si [10-13]. Authors showed that an addition of such metals (Me) as Pt, Pd, Ti, Ta or W to the Ni/Si thin film system results in formation of a monosilicide solution Ni<sub>1-x</sub>Me<sub>x</sub>Si, which increases the monosilicide stability at high temperatures. In contrast, an addition of Co to the Ni-Si system results in formation of a disilicide solid solution Ni<sub>1-x</sub>Co<sub>x</sub>Si<sub>2</sub> and leads to a decrease of the disilicide formation temperature [14-15].

Richter et al. analyzed for the first time the change of  $NiSi_2$  lattice parameters due to a substitution of Si by Al or Ga [16]. The zero lattice match to silicon is reached for the compositions of  $NiSi_{1.74}Al_{0.26}$  and  $NiSi_{1.83}Ga_{0.17}$  at room temperature. This reveals the possibility to fabricate nearly defect-free unstrained epitaxial layers, which is challenging for both technological and fundamental research.

Up to now there are only a few studies on the growth of Ni-Al-Si films [17, 18]. We reported for the first time on the structure of thin NiSi<sub>2-x</sub>Al<sub>x</sub> and NiSi<sub>2-x</sub>Ga<sub>x</sub> layers on Si(001) [19, 20]. In this study we summarize the observed results and discuss the formation of silicide phases, their structure and chemical composition in Ni-Al-Si and Ni-Ga-Si thin film systems. The influence of two parameters is considered: annealing temperature and content of substitution element (Al or Ga).

## **Experimental**

Thin films of different atomic Ni/Al ratios were codeposited onto Si(001) substrates by magnetron sputtering, whereas thin Ni/Ga layers were codeposited by molecular beam epitaxy. In both growth processes Ni and the substitution element (Al or Ga) were deposited simultaneously at room temperature. The amount of the deposited Ni corresponds to a Ni layer thickness of about 20 nm. The amount of deposited Al (resp. Ga) is described by the atomic ratio Ni/Al (resp. Ni/Ga) = 1/z. A detailed description of the growth procedures for Ni/Al and Ni/Ga samples is given in [19] and [20], respectively.

After deposition the Ni/Al as well as Ni/Ga samples were isochronically annealed in a  $N_2$  ambient for 30 s using rapid thermal annealing. The annealing temperature was varied from 450°C to 900°C in steps of 50 degrees. For comparison the same deposition procedures were carried out using a pure Ni film on Si(001).

The layer structure was characterized by transmission electron microscopy (TEM) in plan-view (Si[001]) as well as in cross-section (Si[110]) using a Philips CM20 microscope operating at 200 kV. Scanning transmission electron microscopy (STEM) and electron energy dispersive X-ray spectroscopy (EDXS) were carried out on a JEOL JEM2200FS microscope operating at 200 kV and equipped with a Bruker XFlash 5030 EDS detector.

## **Results**

**Phase formation in Ni-Si system.** Cross-sectional TEM imaging and electron diffraction revealed changes in the microstructure of pure Ni layers on Si(001) after annealing at different temperatures (Fig. 1). Closed polycrystalline monosilicide NiSi layers are formed after annealing at 500°C (Fig. 1a). An annealing at 700°C results in formation of silicide islands. Plan-view electron diffraction analysis shows the simultaneous presence of two silicide phases: polycrystalline NiSi and epitaxial NiSi<sub>2</sub> (Fig. 1b). A complete transformation into the disilicide phase, NiSi<sub>2</sub>, is accomplished after an annealing at 800°C. Samples annealed above 800°C consist of NiSi<sub>2</sub> islands having a shape of

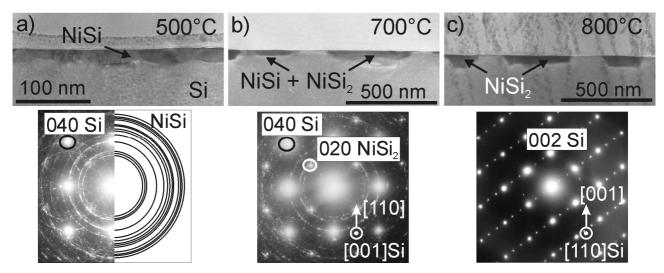


Figure 1: TEM cross-section images and corresponding electron diffraction patterns of Ni deposited onto Si(001) and subsequently annealed at a) 500°C, b) 700°C and c) 900°C. Notice that diffraction patterns in a) and b) were obtained at plan-view specimens, i.e. in the Si[001] zone axis, whereas the diffraction pattern in c) - in the Si[110] cross-section. The white arrows in c) mark diffraction spots appearing due to the presence of B-type twins.

truncated pyramids (Fig. 1c). Electron diffraction analysis in Si[110] cross-section proved the presence of both A- and B-type orientations. These results are in full agreement with literature.

**Phase formation in Ni-Al-Si system.** An addition of Al into the Ni-Si system leads to a drastic change in the layer structure. The NiSi<sub>2</sub> formation temperature decreases down to 500°C. Isolated NiSi<sub>2</sub> crystallites of about 10 nm in size were observed after a 500°C annealing at the NiSi/Si interface already at an Al content z of 0.06 (Fig. 2a). They have a pyramidal shape and exhibit the A-type orientation. AES analysis (not presented here) showed that Al is concentrated only in the upper oxidized layer. An increase of Al content up to z = 0.2 leads to formation of a closed about 50 nm thick NiSi<sub>2-x</sub>Al<sub>x</sub> layer with a rough NiSi<sub>2-x</sub>Al<sub>x</sub>/Si interface (interfacial roughness of about 30 nm). This rough layer appears at the interface between a 20 nm thick polycrystalline NiSi<sub>0.5</sub>Al<sub>0.5</sub> film and Si (Fig. 2b). The presence of the polycrystalline NiSi<sub>0.5</sub>Al<sub>0.5</sub> film was proved by electron diffraction analysis elsewhere [19]. The similar sample structure was observed for Al contents ranging from z = 0.2 to z = 0.35. A further increase of the Al concentration up to z = 0.6 results in formation of a 40 nm thick NiAl layer with a closed NiSi<sub>2-x</sub>Al<sub>x</sub> layer underneath (Fig. 2c). The NiSi<sub>2-x</sub>Al<sub>x</sub> layer thickness decreases down to 30 nm. Its interfacial roughness reaches about 40 nm. The presence of NiAl phase was proved by electron diffraction. According to AES analysis there is an amount of Si solved in the upper NiAl layer. All the samples have an amorphous top layer Al-O.

Figure 3 shows cross-sectional images of  $NiSi_{2-x}Al_x$  layers with different Al contents annealed at 900°C. An annealing of pure Ni results in formation of large pyramidal  $NiSi_2$  islands separated by silicide-free substrate regions. It is visible that addition of Al leads to formation of closed  $NiSi_{2-x}Al_x$  layers. The layer roughness strongly depends on the Al content. An addition of small amount of Al (z = 0.06) leads to formation of a  $NiSi_{2-x}Al_x$  layer with an interfacial roughness reaching up to 80 nm. An increase of the Al content up to z = 0.2...0.3 results in a drastic decrease of the layer roughness down to the nanometer scale. A further increase of the Al content up to z = 0.6 leads to a deterioration of the interface roughness up to 30 nm.

According to electron diffraction analysis all  $NiSi_{2-x}Al_x$  layers have the A-type orientation. No B-type oriented crystallites were observed in  $NiSi_{2-x}Al_x$  layers, whereas pure  $NiSi_2$  layers deposited at the same growth conditions consist of a mixture of both orientations (Fig. 1c).

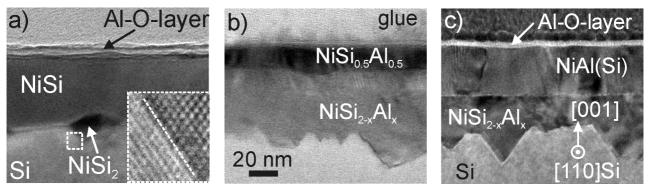


Figure 2: TEM cross-section images of Ni/Al layers codeposited onto Si(001) using different Ni/Al = 1/z ratios a) z = 0.06, b) z = 0.2 and c) z = 0.6 after annealing at 500°C. The inset in a) shows a high-resolution image of an A-type oriented NiSi<sub>2</sub> grain with a NiSi<sub>2</sub>( $1\bar{1}$ 1)/Si( $1\bar{1}$ 1) interface.

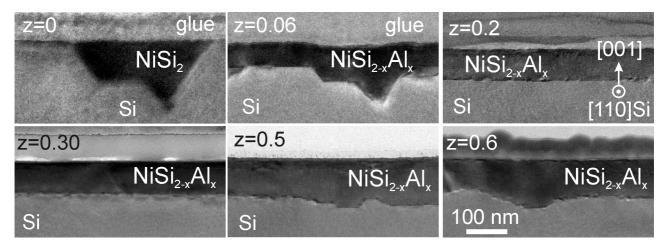


Figure 3: TEM cross-section images of Ni/Al layers codeposited onto Si(001) using different Ni/Al = 1/z ratios and subsequently annealed at 900°C.

**Phase formation in Ni-Ga-Si system.** Similarly to the Ni-Al-Si thin film system an addition of Ga leads to a decrease of the disilicide formation temperature. At a Ga content of z = 0.06...0.17 a mixture of NiSi and NiSi<sub>2-x</sub>Ga<sub>x</sub> with a pure Ga layer on top was observed after annealing at 500°C [20]. Both phases grow polycrystalline.

For a higher Ga content z = 0.22...0.28 epitaxial A-type oriented disilicide grains were observed already after annealing at 450°C. In cross-section they can be recognized by their pyramidal shape (Fig. 4a). It is visible, that the sample consists of a number of layers. Plan-view electron diffraction analysis suggested a possible simultaneous presence of different structures with different compositions. It showed the presence of polycrystalline as well as epitaxial disilicide, textured monosilicide NiSi as well as epitaxial GaNi. Furthermore, formation of a number of different Ga-Ni as well as Ga-Ni-Si compounds is also possible. EDXS analysis was carried out to analyze the layer composition. Figure 4b shows a STEM image of a pyramidal crystallite with the X-ray mappings of Ga, Ni and Si peak intensities. According to this analysis the upper layer contains Ga, Ni and Si. However, the composition of the layer is not uniform. There is the highest Ga content in the 10 nm thick top-layer, whereas a 20 nm thick layer underneath shows a lower Ga content. The pyramidal disilicide grains are formed at the interface to Si. Within the EDXS detection limit no Ga was observed in these pyramidal crystallites. This proves the simultaneous presence of the pure epitaxial NiSi<sub>2</sub> phase.

After annealing at higher substrate temperatures there is a change in the layer structure. Without Ga isolated NiSi<sub>2</sub> crystallites (A- as well as B-type oriented) grow above 800°C predominantly with the NiSi<sub>2</sub>{111}/Si{111} interfaces (Fig. 1c). For z ranging from 0.06 to 0.17 closed NiSi<sub>2-x</sub>Ga<sub>x</sub> layers with a corrugated interface were observed in the temperature interval from 600°C to 750°C (Fig. 5a). The silicide grains do not grow epitaxially. Electron diffraction analysis showed that they

exhibit a  $NiSi_{2-x}Ga_x(110) \parallel Si(001)$  texture. These closed layers break up in the samples annealed at 900°C, which results in the island formation (Fig. 5a). The islands maintain the (110)-texture.

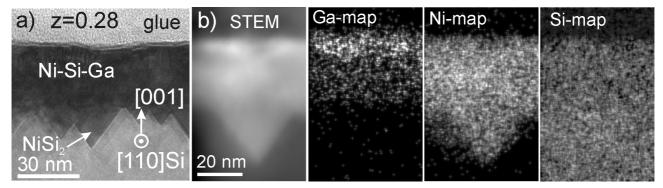


Figure 4: a) Cross-sectional TEM image of the sample structure with a Ga content z = 0.28 after annealing at 450°C. b) STEM image of a pyramidal crystallite with the EDXS mapping of Ga, Ni and Si peak intensities.

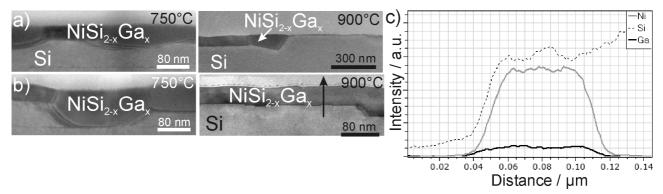


Figure 5: Cross-sectional TEM micrographs of samples with Ga contents a) z = 0.17 and b) z = 0.28 annealed at 750°C and 900°C. c) EDXS line scan across the NiSi<sub>2-x</sub>Ga<sub>x</sub> layer obtained at z = 0.28 and annealed at 900°C (see the black line in the corresponding cross-sectional image).

In contrast, at the higher Ga content of z = 0.22...0.28 two different layer morphologies are present after annealing at 750°C: polycrystalline NiSi<sub>2-x</sub>Ga<sub>x</sub> regions (Fig. 5b) and epitaxial A-type NiSi<sub>2-x</sub>Ga<sub>x</sub> regions [20]. Higher annealing temperatures above 900°C lead to formation of epitaxial A-type NiSi<sub>2-x</sub>Ga<sub>x</sub> layers only (Fig. 5b). EDXS analysis showed a homogeneous Ga distribution in the film (Fig. 5c). The layers exhibit preferentially the NiSi<sub>2-x</sub>Ga<sub>x</sub>(001)/Si(001) interface. However, the interfacial roughness varies locally up to 30 nm due to the presence of the NiSi<sub>2-x</sub>Ga<sub>x</sub>(111) facets. Thus, further experiments are required to optimize the layer roughness.

#### **Summary**

Phase formation in the Ni-Al-Si and Ni-Ga-Si thin film system was investigated. Thin films of different atomic Ni/Al and Ni/Ga ratios were codeposited at room temperature onto Si(001) substrates by magnetron sputtering and molecular beam epitaxy, respectively. Subsequently, the layers were ex-situ annealed at different temperatures.

Addition of Al leads to a decrease of the disilicide formation temperature down to at least 500°C. Without Al separated NiSi<sub>2</sub> islands with the A- and B-type orientations are observed at temperatures above 800°C, whereas addition of Al leads to the formation of closed NiSi<sub>2-x</sub>Al<sub>x</sub> layers. The layer roughness strongly depends on the Al content: almost smooth layers are observed at Al content z ranging from 0.2 to 0.3. Under the presence of Al only A-type oriented NiSi<sub>2-x</sub>Al<sub>x</sub> was observed.

Addition of Ga into the Ni-Si thin film system also results in a decrease of the disilicide nucleation temperature down to less than  $450^{\circ}$ C for the Ga content of z = 0.22...0.28. In contrast to

Al, addition of Ga leads to a change in the disilicide  $NiSi_{2-x}Ga_x$  orientation. Closed layers with a  $NiSi_{2-x}Ga_x(110) \parallel Si(001)$  texture are observed after a 750°C annealing at low Ga contents of z=0.06...0.17. Similarly to the pure Ni-Si system island formation was observed after annealing above 900°C for these Ga concentrations. For higher Ga contents of z=0.22...0.28 closed A-type oriented  $NiSi_{2-x}Ga_x$  layers were observed after the 900°C annealing. These layers exhibit preferentially the  $NiSi_{2-x}Ga_x(001)/Si(001)$  interface. However, there is still a large number of  $NiSi_{2-x}Ga_x\{111\}/Si\{111\}$  facets, which cause an interfacial roughness of about 30 nm.

## References

- [1] L.J. Chen: Silicide Technology for Integrated Circuits (IEE, London 2004).
- [2] C. Lavoie, F.M. d'Heurle, C. Detavernier, C. Cabral Jr.: Microelectronic Engineering Vol. 70 (2003), p. 144
- [3] J.P. Sullivan, R.T. Tung, F. Schrey: J. Appl. Phys. Vol. 72 (1992), p. 478
- [4] J.E.E. Baglin, H.A. Atwater, D. Gupta, F.M. D'Heurle: Thin Solid Films Vol. 93 (1982), p. 255
- [5] M.Y. Lee, P.A. Bennett: Phys. Rev. Lett. Vol. 75 (1995), p. 4460
- [6] K. Tsutsui, R. Xiang, K. Nagahiro, T. Shiozawa, P. Ahmet, Y. Okuno, M. Matsumoto, M. Kubota, K. Kakushima, H. Iwai: Microelectronic Engineering Vol. 85 (2008), p. 315
- [7] D. Hesse, P. Werner, , R. Mattheis, J. Heydenreich: Mat. Res. Soc. Symp. Proc. Vol 320 (1994), p. 221
- [8] K.C.R. Chui, J.M. Poate, J.E. Rowe, T.T. Sheng, A.G. Cullis: Appl. Phys. Lett. Vol. 38 (1981), p. 988
- [9] V. Teodorescu, L. Nistor, H. Bender, A. Steegen, A. Lauwers, K. Maex, J.V. Landuyt: J. Appl. Phys. Vol. 90 (2001), p. 167
- [10] K.L. Pey, P.S. Lee, D. Mangelinck: Thin Solid Films Vol. 462-463 (2004), p. 137
- [11] D. Deduytsche, C. Detavernier, R.L. Van Meirhaeghe, J.L. Jordan-Sweet, C. Lavoie: J. Appl. Phys. Vol. 101 (2007), p. 044508
- [12] X.-P. Qu, Y.-L. Jiang, G.-P. Ru, F. Lu, B.-Z. Li, C. Detavernier, R.L. Van Meirhaeghe: Thin Solid Films Vol. 462-463 (2004), p.146
- [13] C. Detavernier, X.P. Qu, R.L. Van Meirhaeghe, B.Z. Li, K. Maex: J. Mater. Res. Vol.18 (2003), p.1668
- [14] J. Liu, D.L. Kwong: Appl. Phys. Lett. Vol. 88 (2006), p. 052109
- [15] E.J. Jung, S.-W. Jung, H.-S. Kim, J.-H. Yun, S.H. Cheong, B.H. Kim, G.H. Choi, S.T. Kim, U.-I. Chung, J.T. Moon, B.I. Ryu: Microelectronic Engineering Vol. 82 (2005), p. 449
- [16] K.W. Richter, K. Hiebl: Appl. Phys. Lett. Vol. 83 (2003), p. 497
- [17] A. Katz, Y. Komem: J. Vac. Sci. Technol. A Vol. 7 (1989), p. 1484
- [18] A. Katz, Y. Komem: Mat. Res. Soc. Symp. Proc. Vol. 100 (1988), p. 719
- [19] F. Allenstein, L. Budzinski, D. Hirsch, A. Mogilatenko, G. Beddies, R. Grötzschel, H.-J. Hinneberg: Microelectronic Engineering Vol. 82 (2005), p. 474
- [20] A. Mogilatenko, F. Allenstein, M.A. Schubert, M. Falke, G. Beddies, H.-J. Hinneberg, W. Neumann: Phys. Stat. Sol. (c) Vol. 5 (2008), p. 3752